



## CST2319 P-Ch 40V Fast Switching MOSFETs

- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

## CST2319 Product Summary



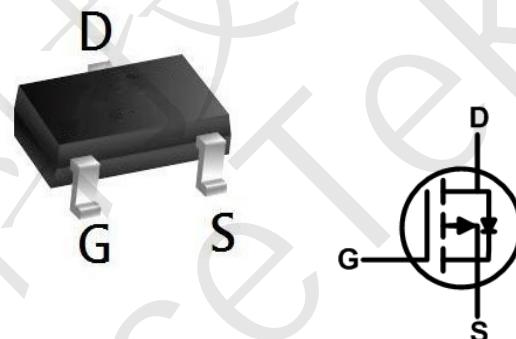
| BVDSS | RDS(ON) | ID   |
|-------|---------|------|
| -40V  | 63mΩ    | -4 A |

## CST2319 Description

The CST2319 is the high cell density trenched N-ch MOSFETs, which provides excellent RDS(ON) and efficiency for most of the small power switching and load switch applications.

The CST2319 meet the RoHS and Green Product requirement with full function reliability approved.

## CST2319 SOT 23 Pin Configurations

CST2319 Absolute Maximum Rating ( $T_A=25^\circ\text{C}$  unless otherwise noted)

| Parameter  | Symbol         | Value      | Units            |
|--|----------------|------------|------------------|
| Drain-Source Voltage                                 | $V_{DS}$       | -40        | V                |
| Gate-Source Voltage                                  | $V_{GS}$       | $\pm 20$   | V                |
| Continuous Drain Current<br>$T_A = 25^\circ\text{C}$ | $I_D$          | -4         | A                |
| Pulsed Drain Current <sup>1</sup>                    | $I_{DM}$       | -20        | A                |
| Power Dissipation<br>$T_A = 25^\circ\text{C}$        | $P_D$          | 1.2        | W                |
| Operating Junction and Storage Temperature Range     | $T_J, T_{STG}$ | -55 to 150 | $^\circ\text{C}$ |

## CST2319 Thermal Characteristics

| Parameter  | Symbol          | Value | Units                     |
|--|-----------------|-------|---------------------------|
| Thermal Resistance from Junction to Ambient <sup>2</sup> | $R_{\theta JA}$ | 104   | $^\circ\text{C}/\text{W}$ |



## CST2319 P-Ch 40V Fast Switching MOSFETs

CST2319 Electrical Characteristics ( $T_J=25^\circ\text{C}$  unless otherwise noted)

| Parameter                                      | Symbol                      | Conditions   | Min. | Typ. | Max.      | Units            |
|--|-----------------------------|--|------|------|-----------|------------------|
| <b>Static Characteristics</b>                  |                             |  |      |      |           |                  |
| Drain-Source Breakdown Voltage                 | $V_{(\text{BR})\text{DSS}}$ | $V_{\text{GS}} = 0\text{V}, I_{\text{D}} = -250\mu\text{A}$                                | -40  | -    | -         | V                |
| Zero Gate Voltage Drain Current                | $I_{\text{DSS}}$            | $V_{\text{DS}} = -40\text{V}, V_{\text{GS}} = 0\text{V}$                                   | -    | -    | -1        | $\mu\text{A}$    |
| Gate-Body Leakage                              | $I_{\text{GSS}}$            | $V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$                                | -    | -    | $\pm 100$ | nA               |
| Gate-Threshold Voltage                         | $V_{\text{GS}(\text{th})}$  | $V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = -250\mu\text{A}$                            | -1.2 | -1.5 | -2.5      | V                |
| Drain-Source on-Resistance <sup>3</sup>        | $R_{\text{DS}(\text{on})}$  | $V_{\text{GS}} = -10\text{V}, I_{\text{D}} = -5\text{A}$                                   | -    | 63   | 85        | $\text{m}\Omega$ |
|  |                             | $V_{\text{GS}} = -4.5\text{V}, I_{\text{D}} = -4\text{A}$                                  | -    | 80   | 125       |                  |
| <b>Dynamic Characteristics<sup>4</sup></b>     |                             |  |      |      |           |                  |
| Input Capacitance                              | $C_{\text{iss}}$            | $V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = -20\text{V}, f = 1.0\text{MHz}$                | -    | 553  | -         | pF               |
| Output Capacitance                             | $C_{\text{oss}}$            |  | -    | 50   | -         |                  |
| Reverse Transfer Capacitance                   | $C_{\text{rss}}$            |  | -    | 42   | -         |                  |
| <b>Switching Characteristics<sup>4</sup></b>   |                             |  |      |      |           |                  |
| Total Gate Charge                              | $Q_g$                       | $V_{\text{GS}} = -10\text{V}, V_{\text{DS}} = -20\text{V}, I_{\text{D}} = -5\text{A}$      | -    | 11.8 | -         | nC               |
| Gate-Source Charge                             | $Q_{\text{gs}}$             |  | -    | 2.2  | -         |                  |
| Gate-Drain Charge                              | $Q_{\text{gd}}$             |  | -    | 3    | -         |                  |
| Turn-on Delay Time                             | $t_{\text{d}(\text{on})}$   | $V_{\text{DS}} = -20\text{V}, V_{\text{GS}} = -10\text{V}, R_L = 2.5\Omega, R_G = 3\Omega$ | -    | 7    | -         | ns               |
| Rise Time                                      | $t_r$                       |  | -    | 6.5  | -         |                  |
| Turn-off Delay Time                            | $t_{\text{d}(\text{off})}$  |  | -    | 24   | -         |                  |
| Fall Time                                      | $t_f$                       |  | -    | 7.8  | -         |                  |
| <b>Drain-Source Body Diode Characteristics</b> |                             |  |      |      |           |                  |
| Body Diode voltage <sup>3</sup>                | $V_{\text{DS}}$             | $I_s = -5\text{A}, V_{\text{GS}} = 0\text{V}$  | -    | -    | -1.2      | V                |
| Continuous Source Current                      | $I_s$                       |  | -    | -    | -4        | A                |

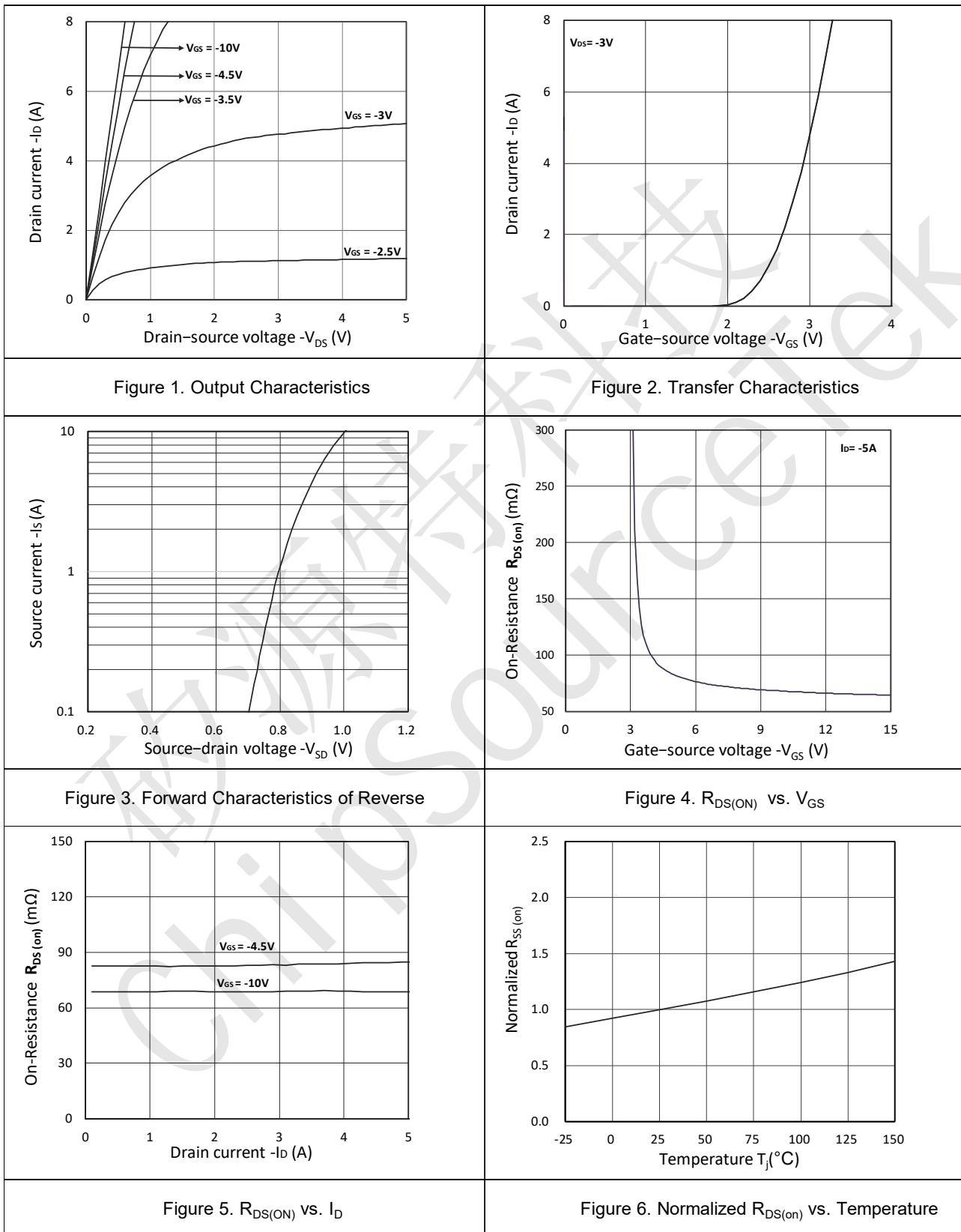
**Notes:**

1. Repetitive rating, pulse width limited by junction temperature  $T_{J(\text{MAX})}=150^\circ\text{C}$ .
2. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
3. Pulse Test: Pulse width $\leq 300\mu\text{s}$ , duty cycle $\leq 2\%$ .
4. This value is guaranteed by design hence it is not included in the production test.



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## CST2319 Typical Characteristics





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ShenZhen ChipSourceTek Technology Co. ,Ltd.

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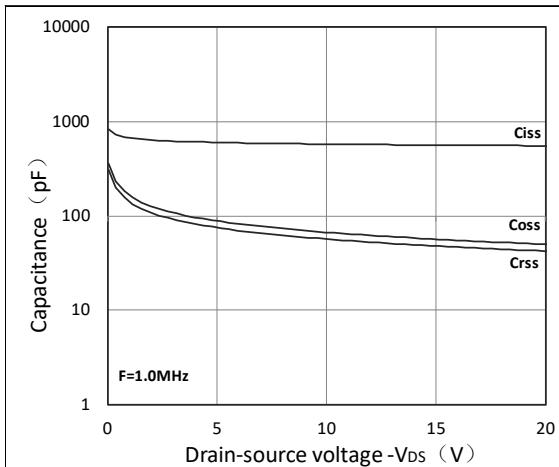


Figure 7. Capacitance Characteristics

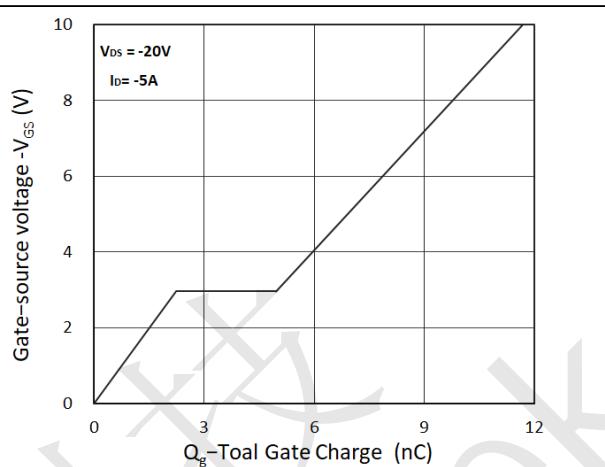


Figure 8. Gate Charge Characteristics

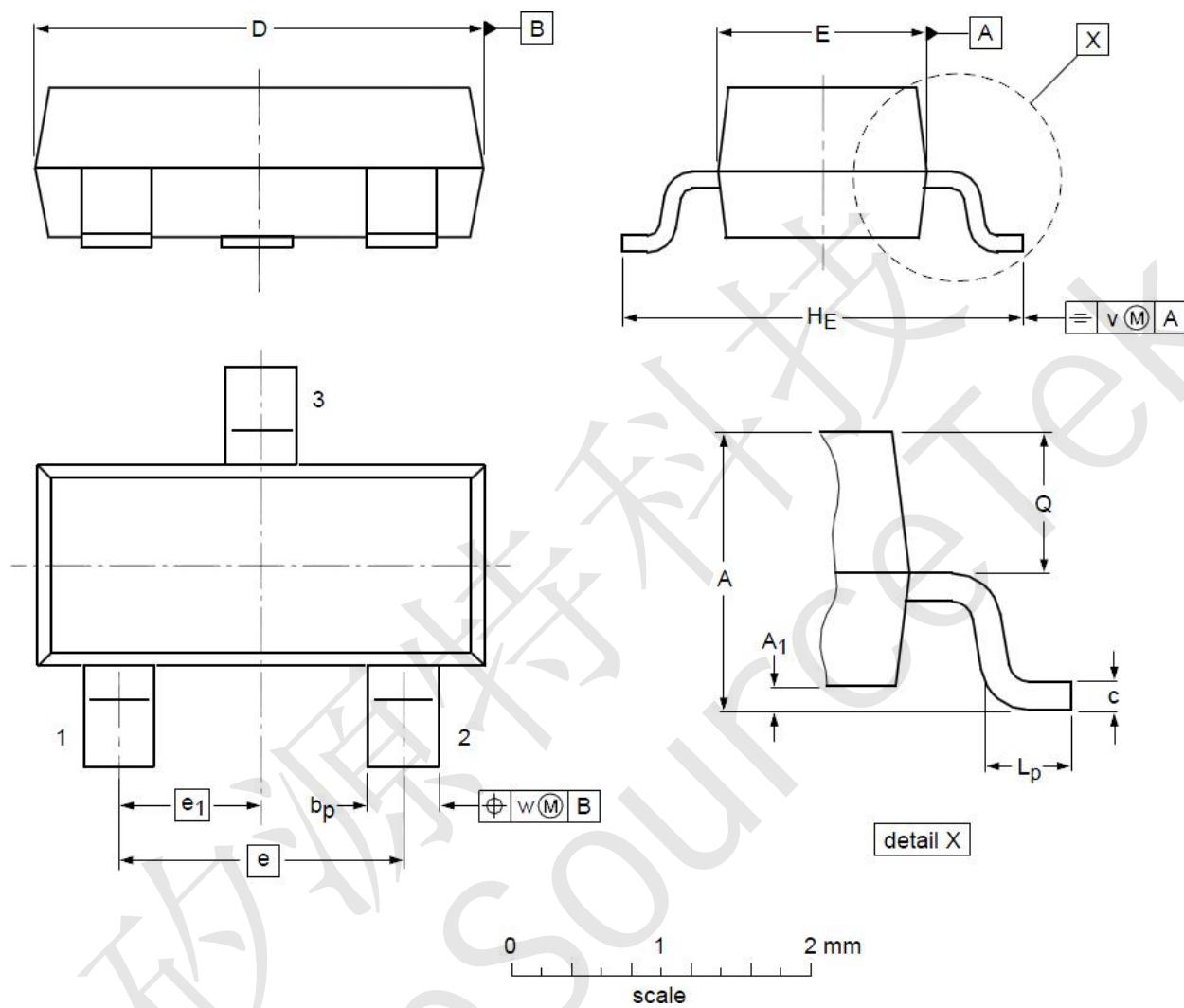


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#### CST2319 SOT23 Mechanical Data



DIMENSIONS ( unit : mm )

| Symbol         | Min  | Typ  | Max  | Symbol         | Min  | Typ  | Max  |
|----------------|------|------|------|----------------|------|------|------|
| A              | 0.90 | 1.01 | 1.15 | A <sub>1</sub> | 0.01 | 0.05 | 0.10 |
| b <sub>p</sub> | 0.30 | 0.42 | 0.50 | c              | 0.08 | 0.13 | 0.15 |
| D              | 2.80 | 2.92 | 3.00 | E              | 1.20 | 1.33 | 1.40 |
| e              | --   | 1.90 | --   | e <sub>1</sub> | --   | 0.95 | --   |
| HE             | 2.25 | 2.40 | 2.55 | L <sub>p</sub> | 0.30 | 0.42 | 0.50 |
| Q              | 0.45 | 0.49 | 0.55 | v              | --   | 0.20 | --   |
| w              | --   | 0.10 | --   |                |      |      |      |