

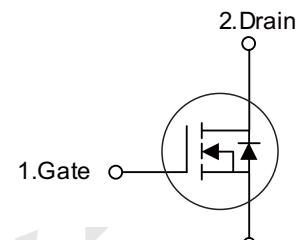


MOT120N03D/C N-CHANNEL MOSFET

■ MOT120N03D/C PRODUCT CHARACTERISTICS

VDSS	30V
R _{DS(on)} Typ(@V _{GS} = 10 V)	3mΩ
ID	120A

Symbol

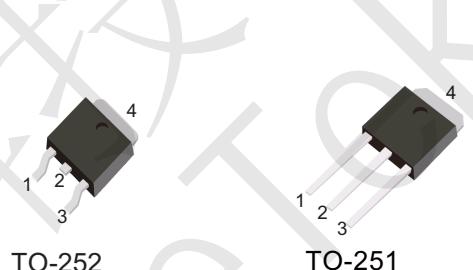


■ MOT120N03D/C APPLICATIONS

- * Power switching application
- * Hard switched and high frequency circuits
- * Uninterruptible power supply

■ MOT120N03D/C FEATURES

- * High density cell design for ultra low Rdson
- * Excellent package for good heat DISSIPATION



■ MOT120N03D/C ORDER INFORMATION

Order codes		Package	Packing
Halogen-Free	Halogen		
N/A	MOT120N03D	TO-252	2500 pieces /Reel
N/A	MOT120N03C	TO-251	70 pieces/Tube

■ MOT120N03D/C ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	120	A
Drain Current-Continuous(T _C =100°C)	I _D (100°C)	84	A
Pulsed Drain Current	I _{DM}	420	A
Maximum Power Dissipation	P _D	120	W
Single pulse avalanche energy ^(Note 5)	E _{AS}	350	mJ
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 To 175	°C

■ MOT120N03D/C THERMAL DATA

Parameter	Symbol	Value	Unit
Thermal Resistance,Junction-to-Case ^(Note 2)	R _{θJC}	1.25	°C/W



MOT120N03D/C N-CHANNEL MOSFET

■ MOT120N03D/C ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1	1.6	3	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=20\text{A}$		3	3.5	$\text{m}\Omega$
Gate resistance	R_G	$F=1.0\text{MHz}$	-	1.2	-	Ω
Forward Transconductance	g_{FS}	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=20\text{A}$	50	-	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	C_{iss}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$		4120		PF
Output Capacitance	C_{oss}			498		PF
Reverse Transfer Capacitance	C_{rss}			456		PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=20\text{V}$ $R_L=0.75\Omega, R_{\text{GEN}}=3\Omega$	-	11	-	nS
Turn-on Rise Time	t_r		-	10	-	nS
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	38	-	nS
Turn-Off Fall Time	t_f		-	11	-	nS
Total Gate Charge	Q_g	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=15\text{V}, I_{\text{D}}=20\text{A}$		79		nC
Gate-Source Charge	Q_{gs}			9		nC
Gate-Drain Charge	Q_{gd}			18		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=20\text{A}$	-	-	1.2	V
Diode Forward Current (Note 2)	I_{S}	-		-	120	A
Reverse Recovery Time	t_{rr}	$T_J = 25^\circ\text{C}, I_F = 20\text{A}$ $di/dt = 100\text{A}/\mu\text{s}$ (Note 3)	-	58	-	nS
Reverse Recovery Charge	Q_{rr}		-	115	-	nC
Forward Turn-On Time	t_{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. EAS condition: $T_j=25^\circ\text{C}, V_{\text{DD}}=15\text{V}, V_{\text{G}}=10\text{V}, L=0.5\text{mH}, R_g=25\Omega$



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■ MOT120N03D/C TYPICAL CHARACTERISTICS

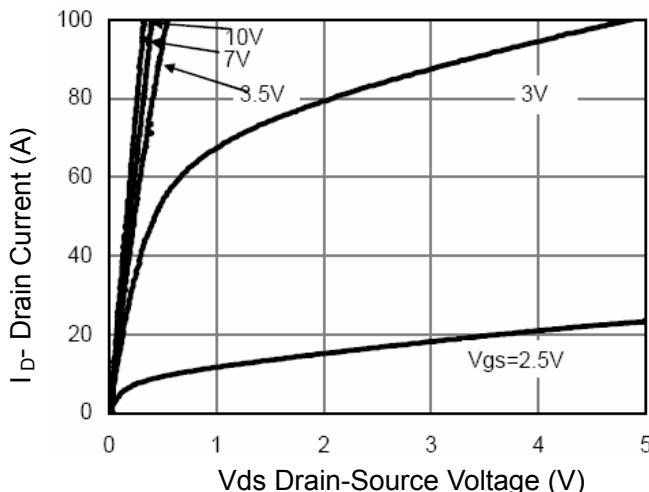


Figure 1 Output Characteristics

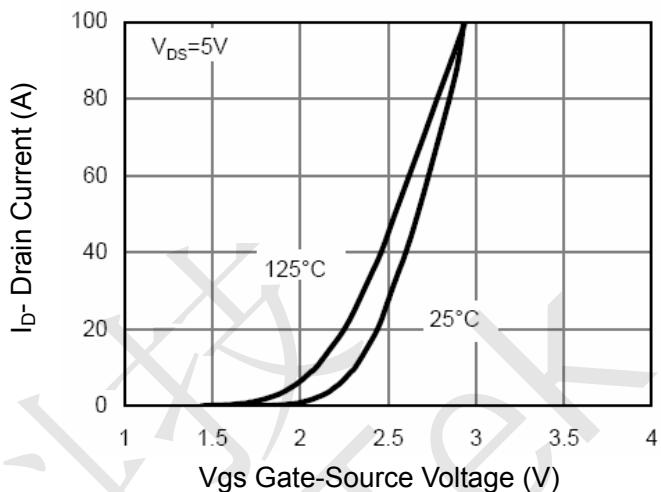


Figure 2 Transfer Characteristics

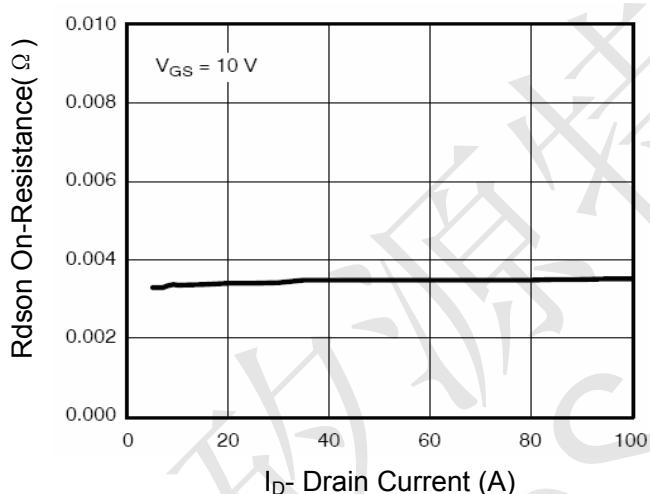


Figure 3 Rdson- Drain Current

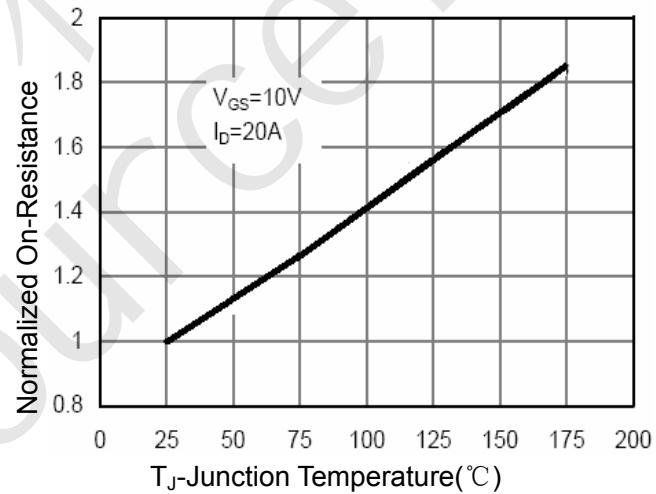


Figure 4 Rdson-Junction Temperature

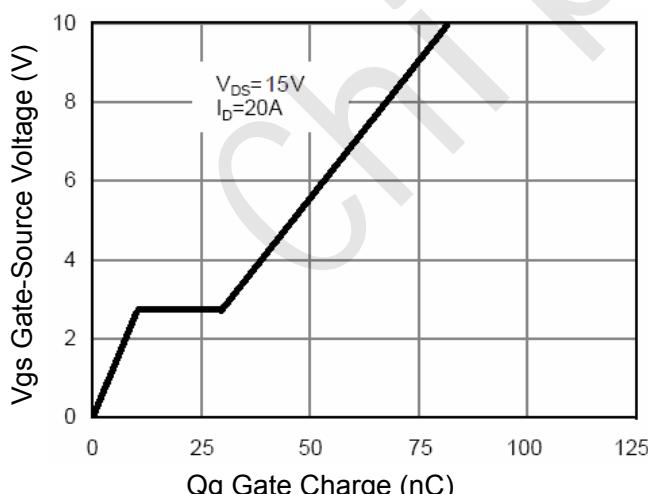


Figure 5 Gate Charge

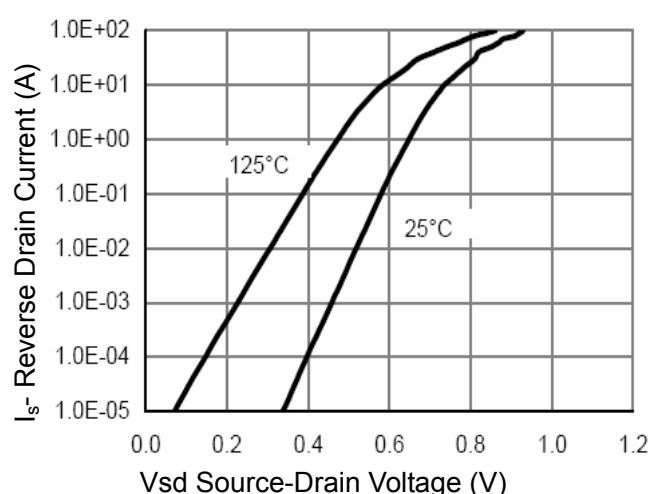


Figure 6 Source- Drain Diode Forward



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■ MOT120N03D/C TYPICAL CHARACTERISTICS(Cont.)

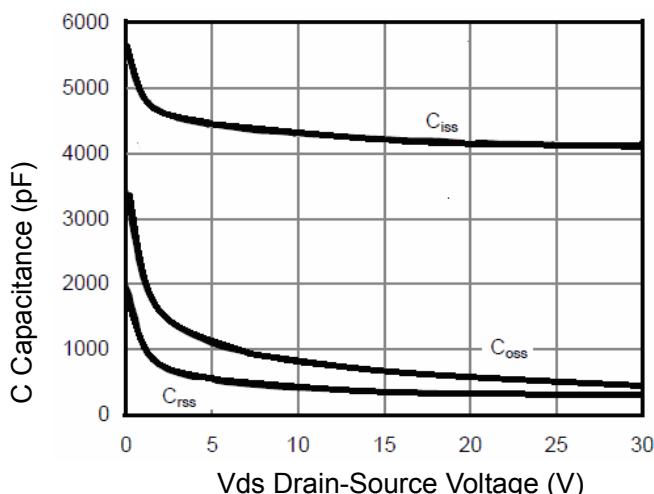


Figure 7 Capacitance vs Vds

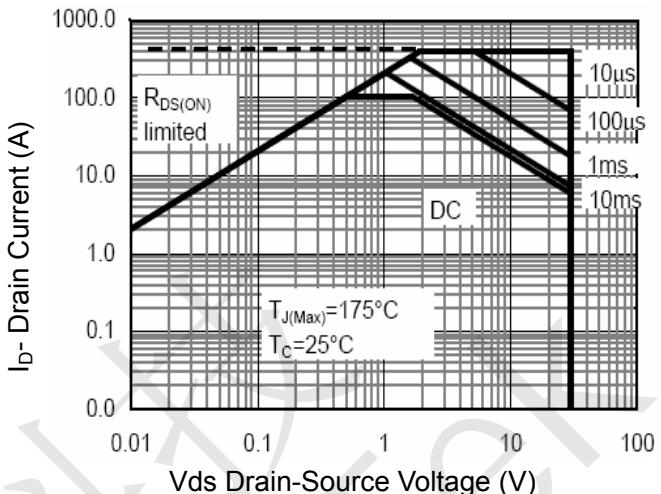


Figure 8 Safe Operation Area

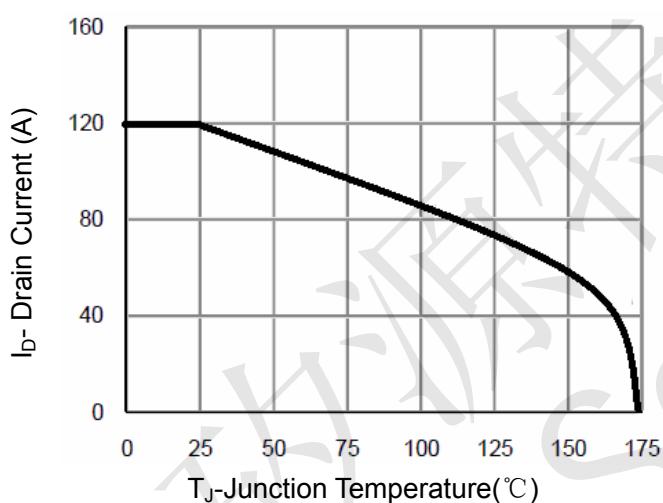


Figure 9 Current De-rating

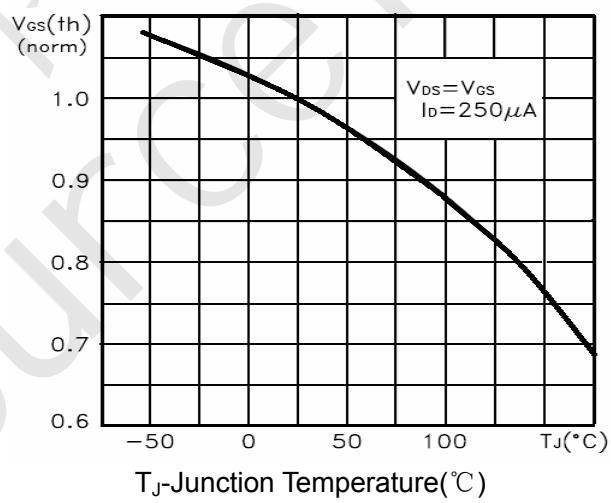


Figure 10 $V_{GS(th)}$ vs Junction Temperature

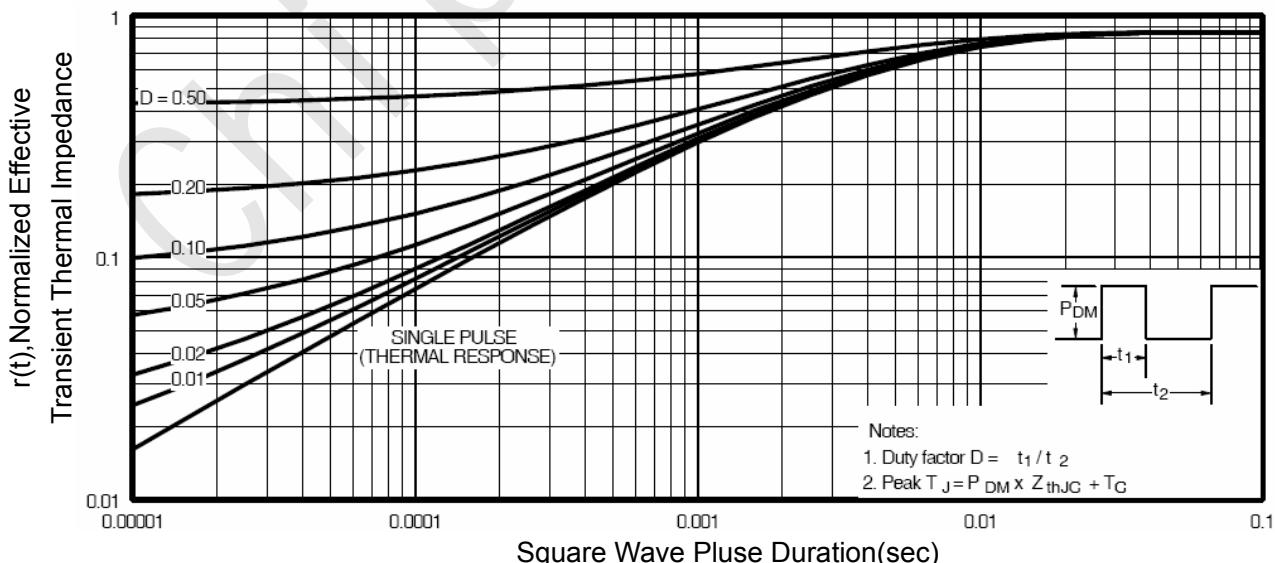


Figure 11 Normalized Maximum Transient Thermal Impedance

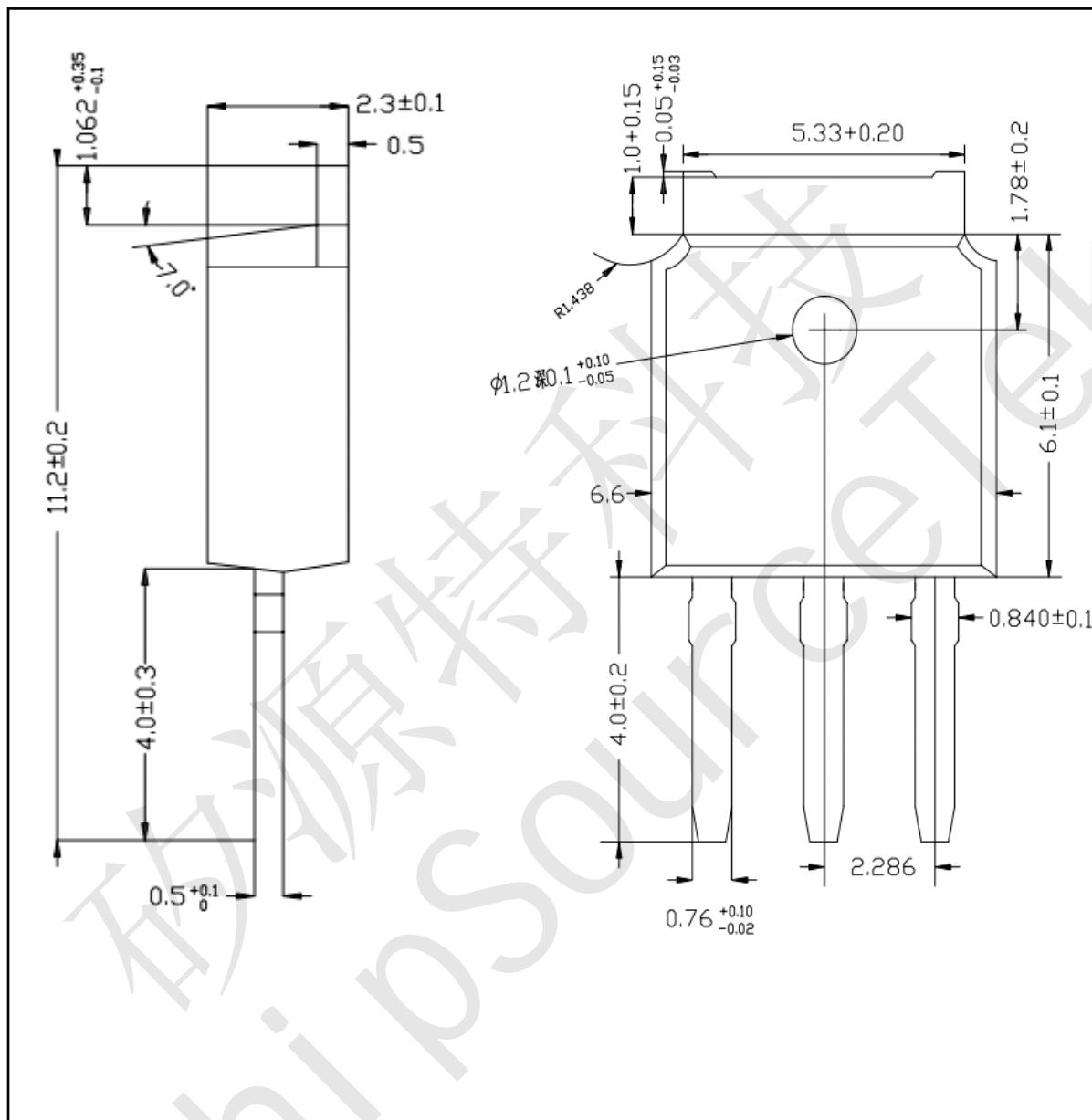


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MOT120N03D/C N-CHANNEL MOSFET

■ MOT120N03D/C TO-251-3L PACKAGE OUTLINE DIMENSIONS





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MOT120N03D/C N-CHANNEL MOSFET

■ MOT120N03D/C TO-252 PACKAGE OUTLINE DIMENSIONS

