



## N-Channel Enhancement Mode Power MOSFET

### Description

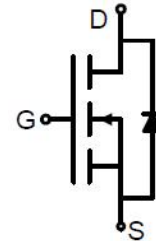
The PE8335M uses advanced trench technology to provide excellent  $R_{DS(ON)}$  and low gate charge. It can be used in a wide variety of applications.

### General Features

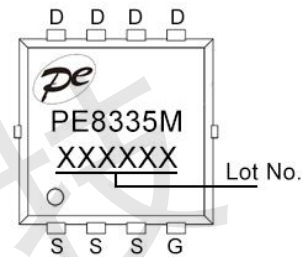
- $V_{DS} = 30V$ ,  $I_D = 35A$   
 $R_{DS(ON)} < 7.5m\Omega @ V_{GS}=10V$   
 $R_{DS(ON)} < 14m\Omega @ V_{GS}=4.5V$
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

### Application

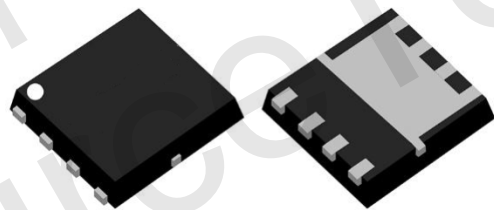
- PWM applications
- Load switch



Schematic diagram



Marking and pin assignment



PDFN3.3x3.3-8L

### Absolute Maximum Ratings (TC=25°C unless otherwise noted)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	35	A
Drain Current-Continuous ( $T_C=100^\circ C$ )	$I_D(T_C=100^\circ C)$	23	A
Pulsed Drain Current (Note 1)	$I_{DM}$	100	A
Maximum Power Dissipation	$P_D$	18	W
Avalanche Energy ( $L=0.1mH$ )	$E_{AS}$	64	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 175	$^\circ C$

### Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	$R_{\theta JC}$	7	$^\circ C/W$
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**Electrical Characteristics (TA=25°C unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	30	-	-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=30V, V_{GS}=0V$	-	-	1	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	$\pm 100$	nA
<b>On Characteristics (Note 3)</b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.6	2.2	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=20A$	-	6	7.5	m $\Omega$
		$V_{GS}=4.5V, I_D=16A$	-	10	14	m $\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS}=5V, I_D=20A$	15	-	-	S
<b>Dynamic Characteristics (Note 4)</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=15V, V_{GS}=0V,$ $F=1.0MHz$	-	1300	-	pF
Output Capacitance	$C_{oss}$		-	140	-	pF
Reverse Transfer Capacitance (Note 4)	$C_{riss}$		-	130	-	pF
Gate Resistance	$R_g$	$V_{DS}=0V, V_{GS}=0V, F=1.0MHz$	-	2	-	$\Omega$
<b>Switching Characteristics</b>						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=15V, I_D=2A, R_L=1\Omega,$ $V_{GS}=10V, R_G=3\Omega$	-	10	-	nS
Turn-on Rise Time	$t_r$		-	8	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	30	-	nS
Turn-Off Fall Time	$t_f$		-	5	-	nS
Total Gate Charge	$Q_g$	$V_{DS}=10V, I_D=25A, V_{GS}=10V$	-	23	-	nC
Gate-Source Charge	$Q_{gs}$		-	7	-	nC
Gate-Drain Charge	$Q_{gd}$		-	4.5	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	$V_{SD}$	$V_{GS}=0V, I_S=10A$	-	-	1.2	V
Diode Forward Current (Note 2)	$I_S$		-	-	40	A

**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to product.



### Typical Electrical and Thermal Characteristics



Figure 1 Switching Test Circuit



Figure 2 Switching Waveform

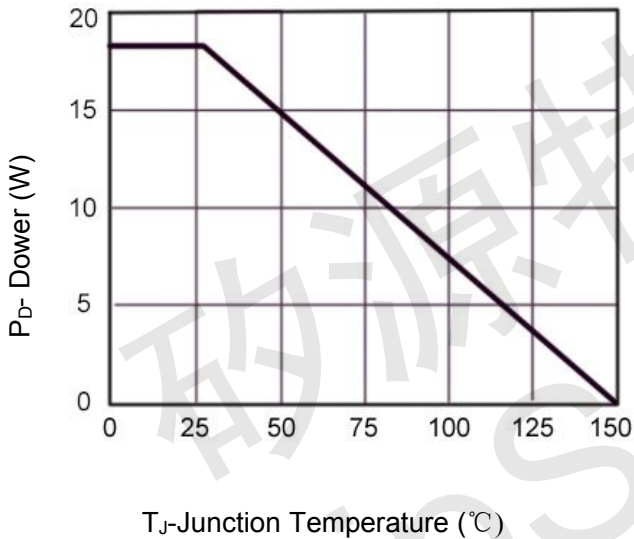


Figure 3 Power De-rating

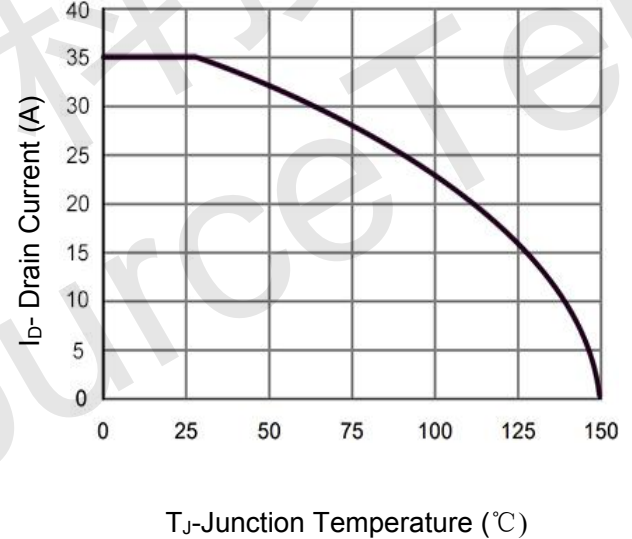


Figure 4 Drain Current

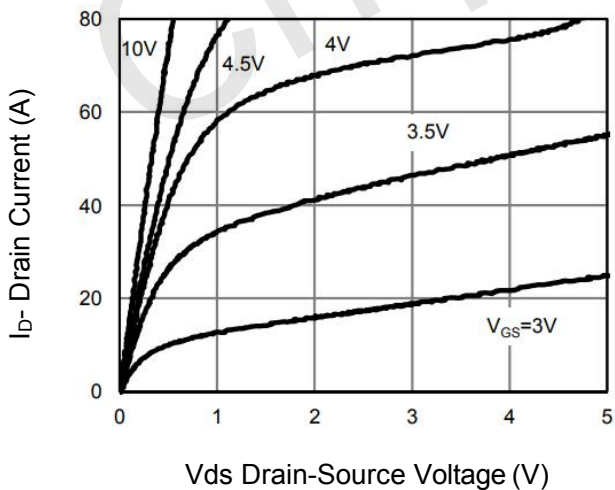


Figure 5 Output Characteristics

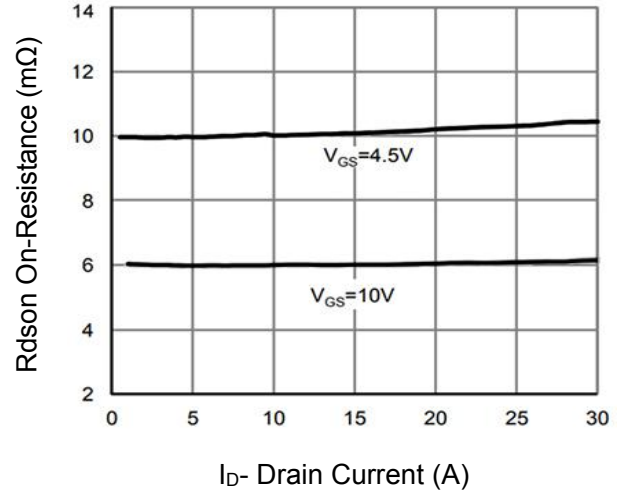
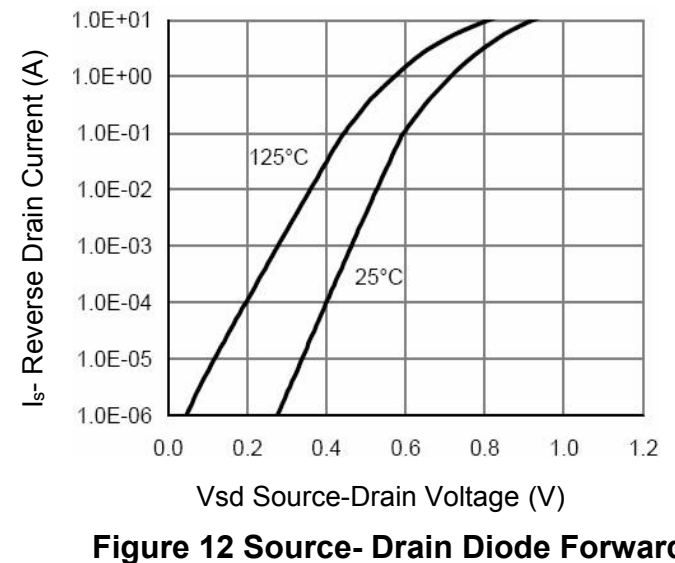
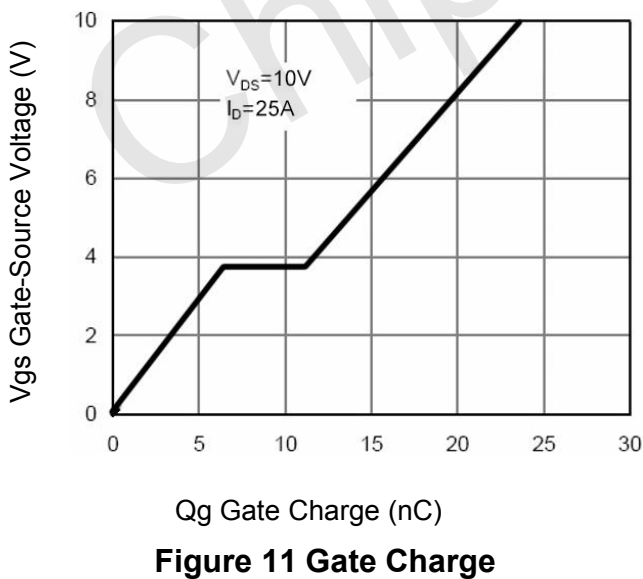
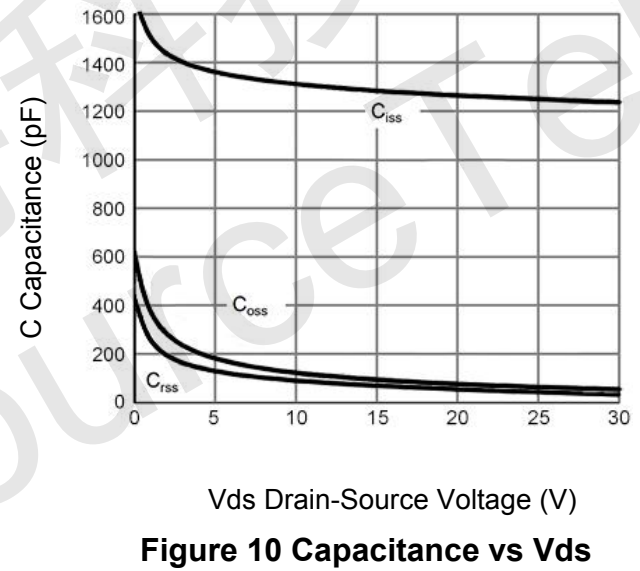
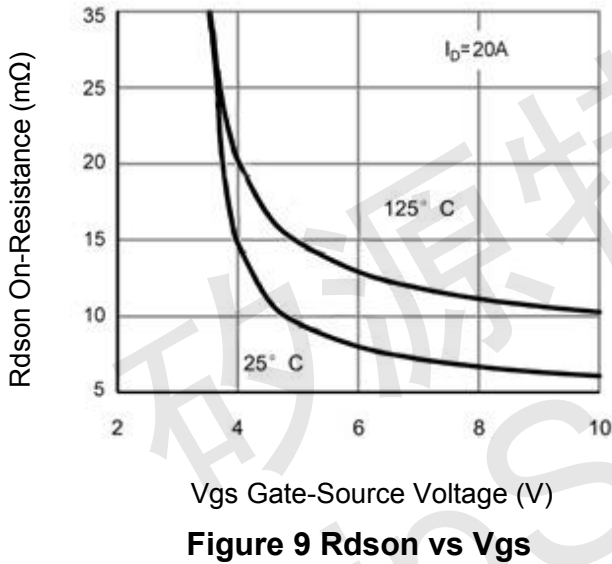
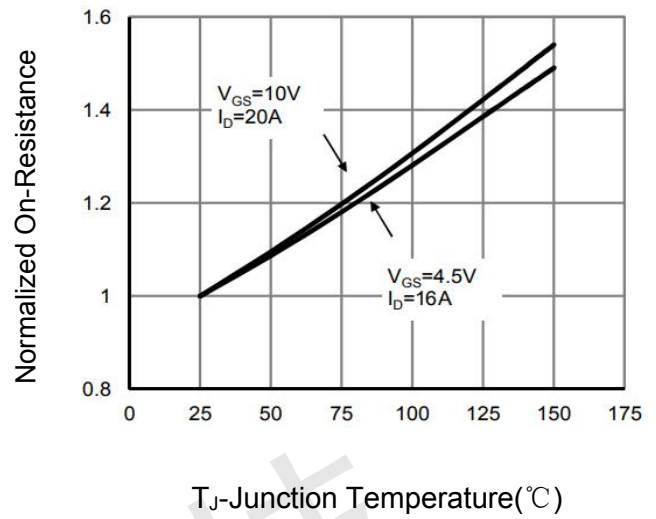
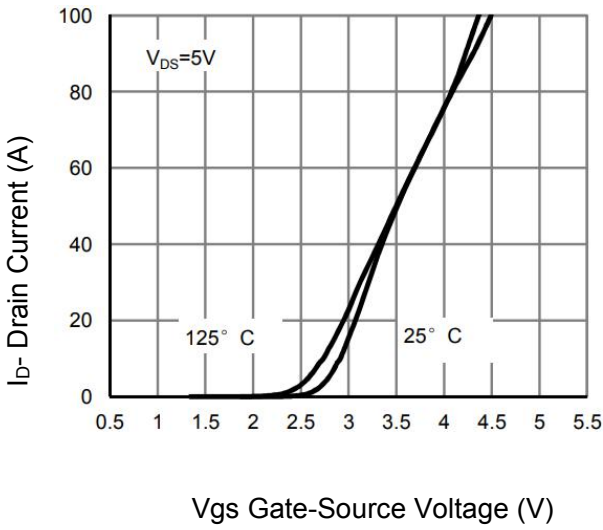


Figure 6 Rdson vs Drain Current



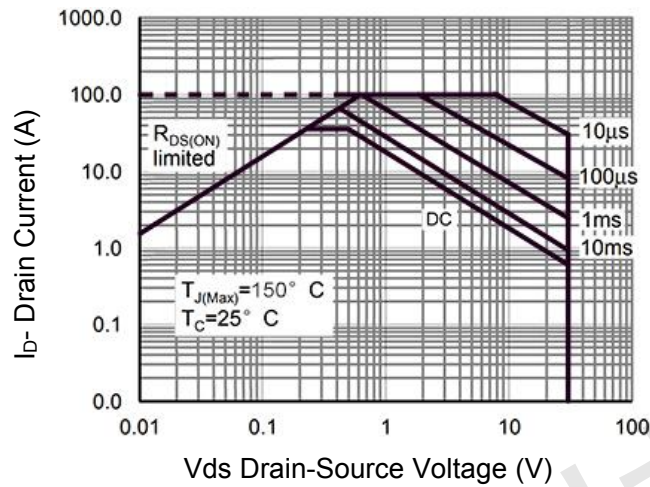


Figure 13 Safe Operation Area

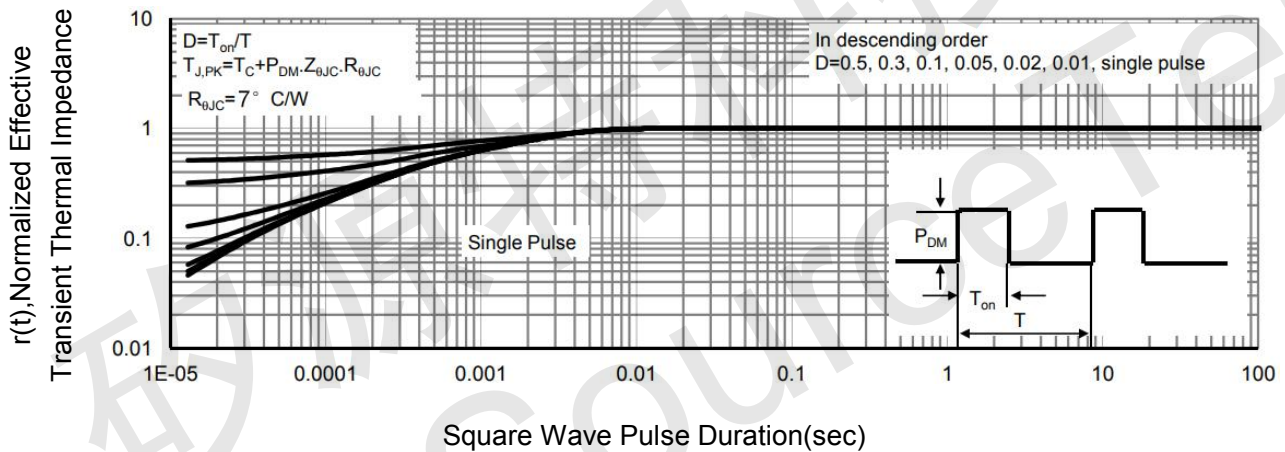
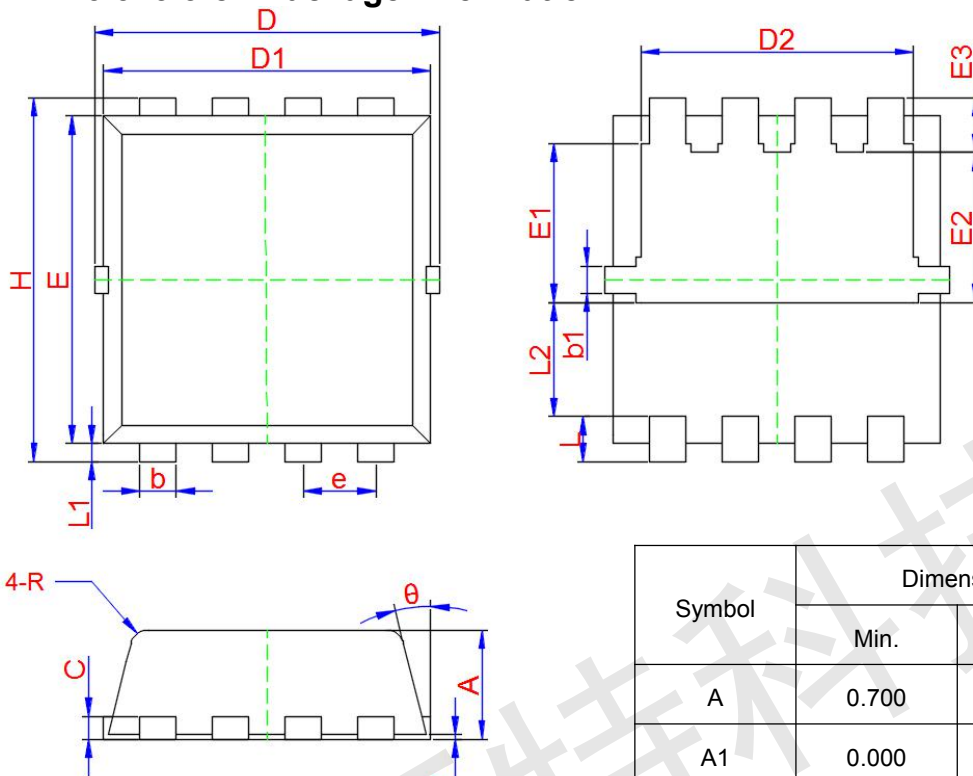


Figure 14 Normalized Maximum Transient Thermal Impedance





**PDFN3.3x3.3-8L Package Information**



Symbol	Dimensions In Millimeters		
	Min.	Typ.	Max.
A	0.700	0.800	0.900
A1	0.000	0.030	0.050
b	0.240	0.300	0.350
b1	0.080	0.130	0.180
c	0.152 TYP.		
D	3.250	3.320	3.400
D1	3.050	3.150	3.250
D2	2.400	2.500	2.600
E	3.000	3.100	3.200
E1	1.350	1.450	1.550
E2	1.200	1.300	1.400
E3	0.400	0.500	0.600
e	0.650 TYP.		
H	3.200	3.300	3.400
L	0.300	0.400	0.500
L1	0.100	0.150	0.200
L2	1.130 TYP.		
R	0.200 TYP.		
θ	6°	10°	14°